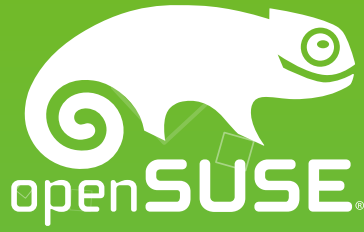

Implementing LoRa, FSK & Further LPWAN Interfaces

Linux Plumbers Conference 2019
You, Me and IoT Microconference

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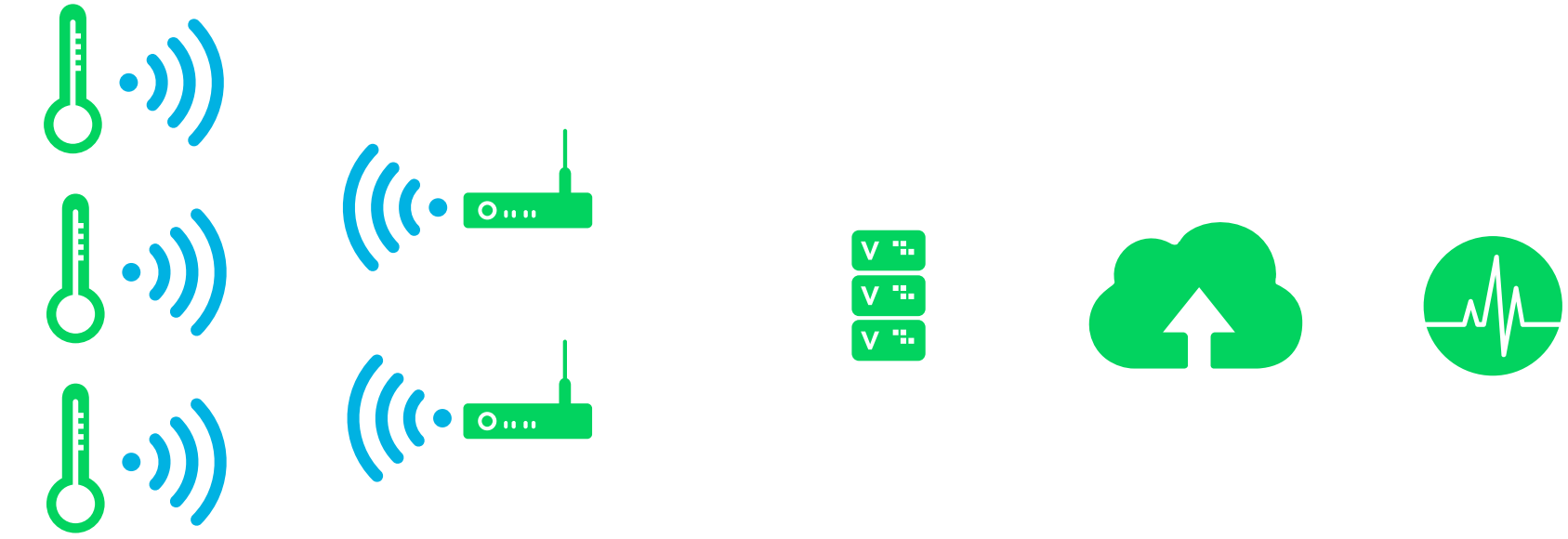
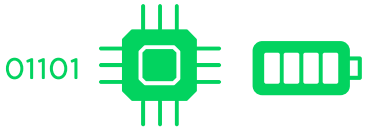
IoT Architecture and Properties

IoT Tiers With Low-Power WAN

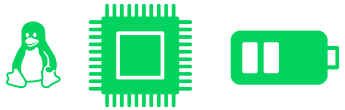


LPWAN Characteristics (1/2)

Low Power



up to 10 years

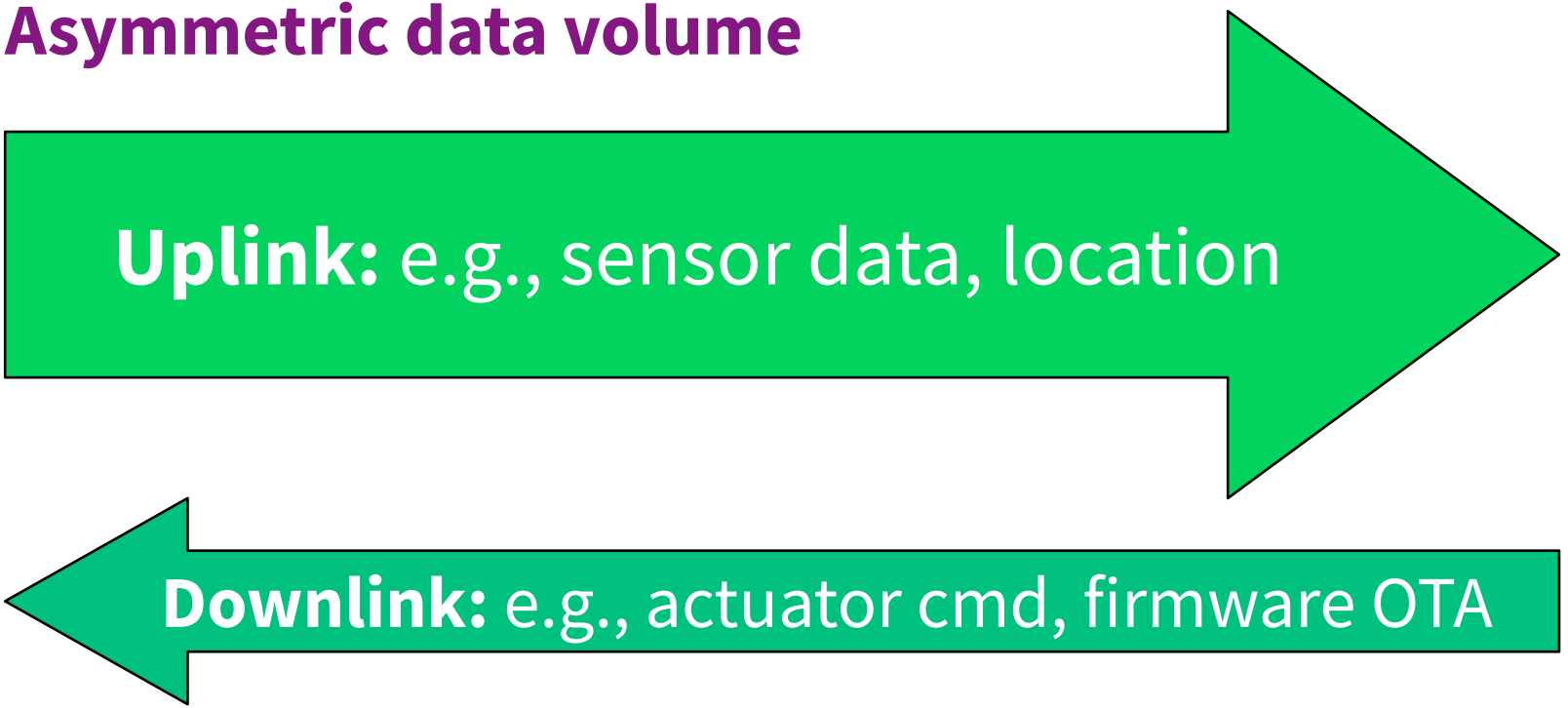


Wide Area

up to 48 km (30 miles)

LPWAN Characteristics (2/2)

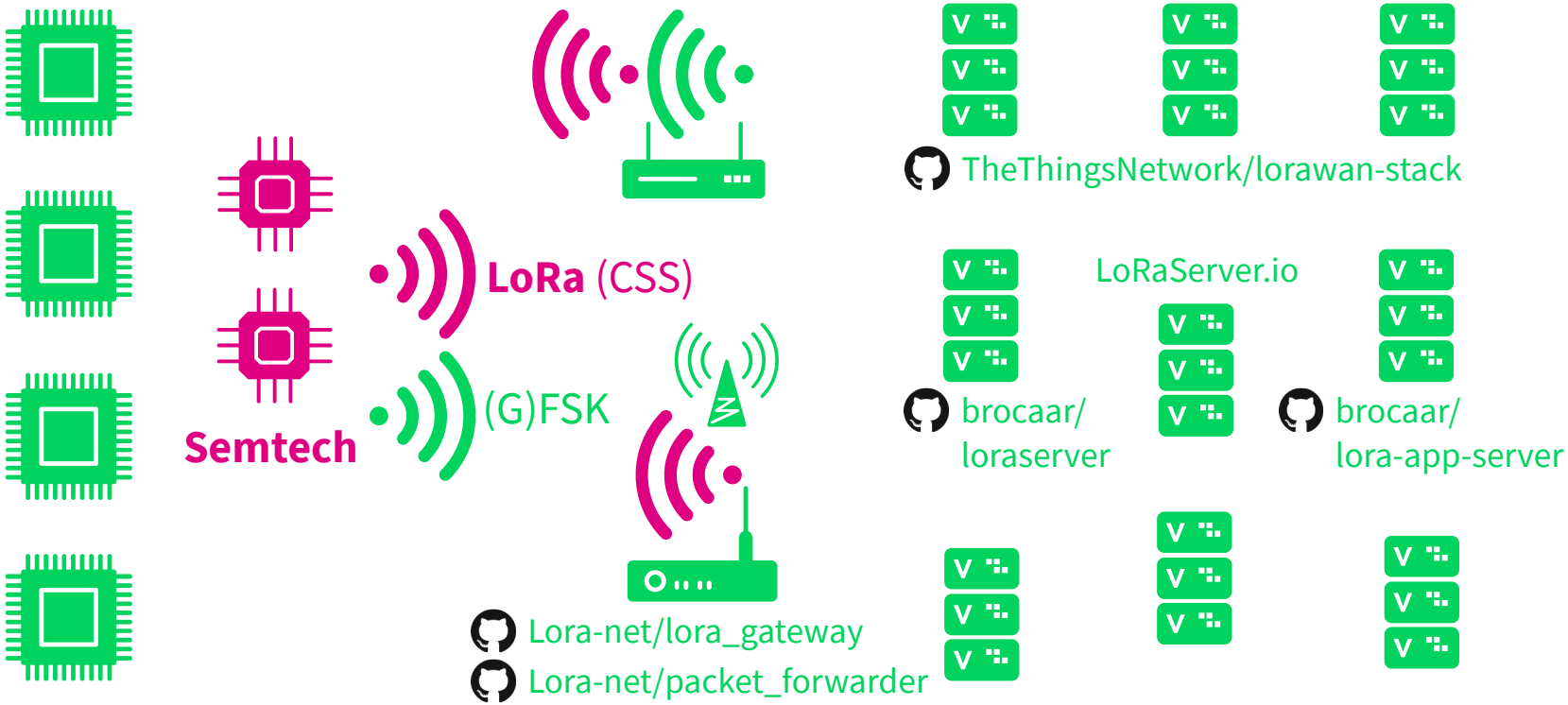
Asymmetric data volume



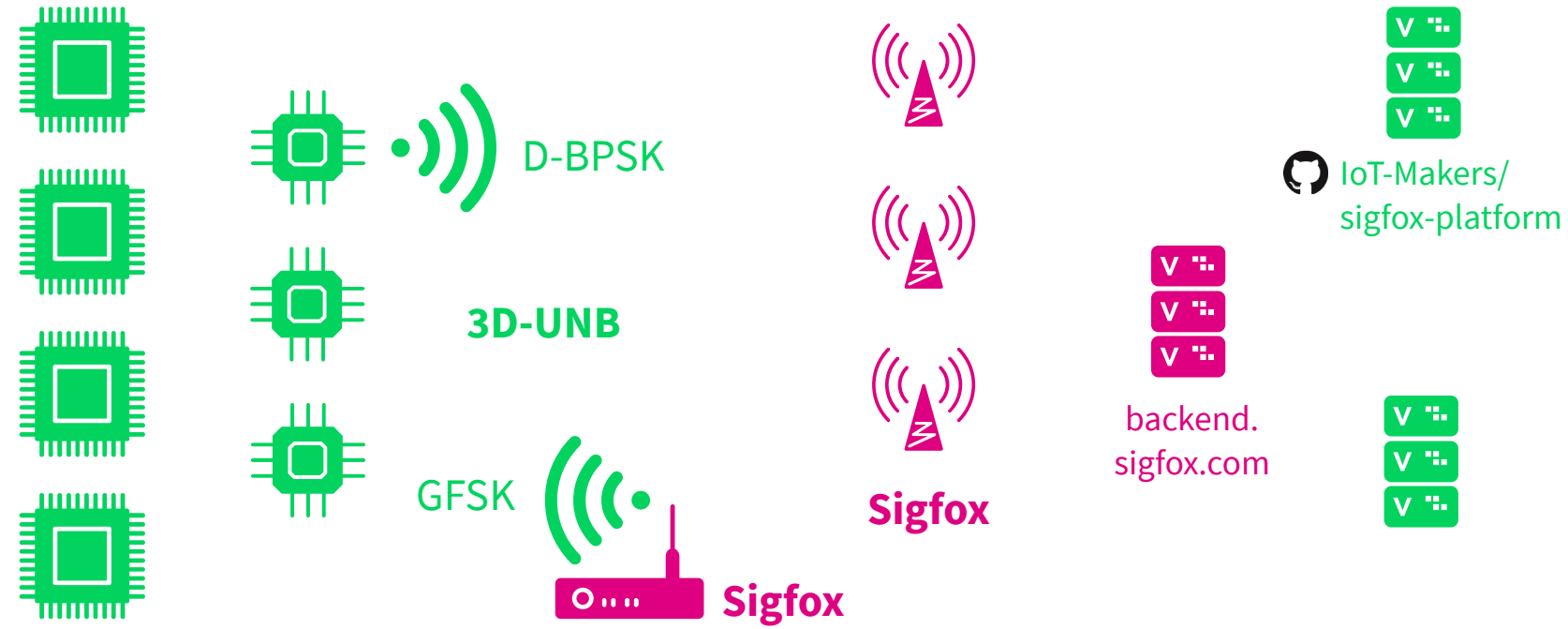
LPWAN Classification

- **Unlicensed** (U-LPWA) – ISM/SRD
  - Sub-GHz (433 / 470 / 868 / 915 / 923 MHz)
  - 2.4 GHz
  - Regulatory restrictions: duty cycle, dwell time
- **Licensed**
  - LTE

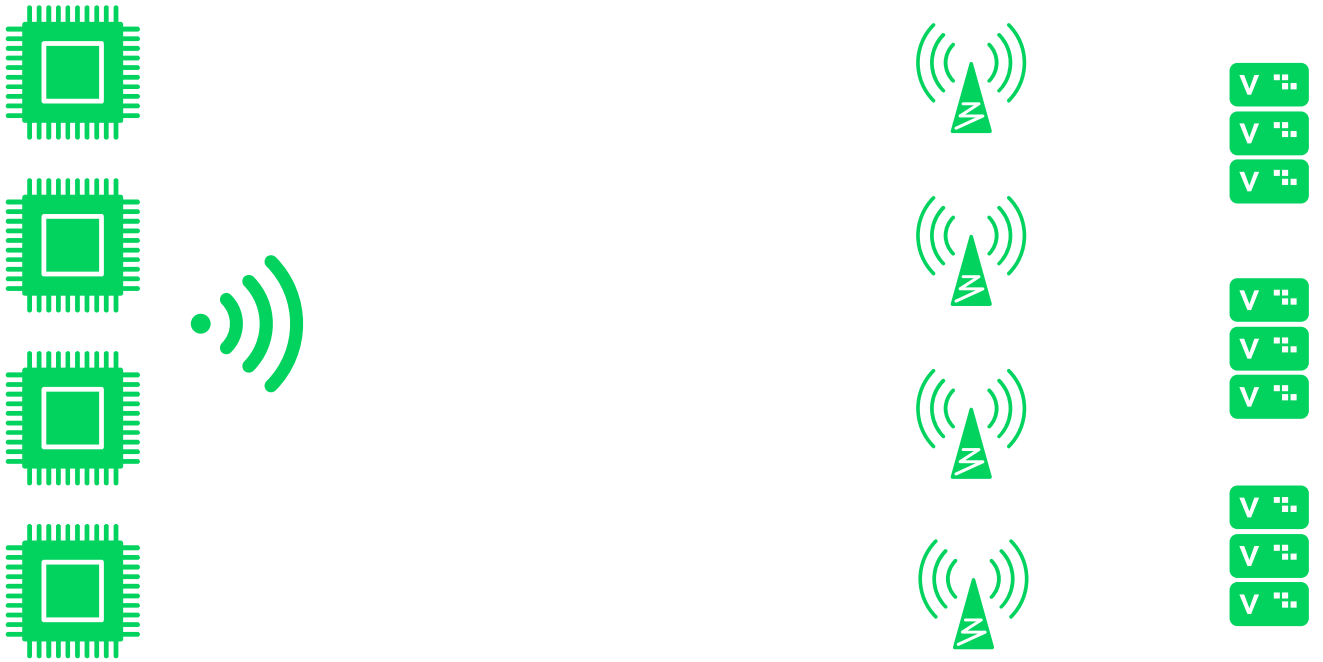
LoRaWAN Architecture



Sigfox Network Architecture

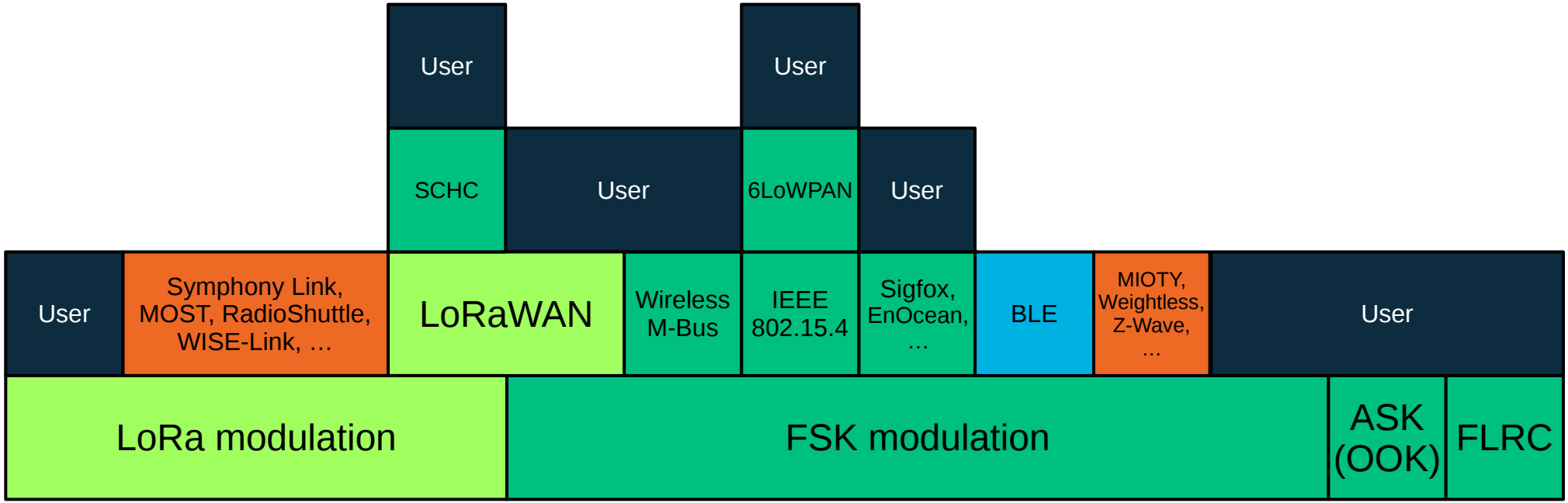


NB-IoT Network Architecture



Representing IoT in Linux

Protocol Layers Around LoRa



Socket Address/Protocol Families

- AF_BLUETOOTH, AF_IEEE802154
- *AF_LORA?* → PF_PACKET
- *AF_**LORAWAN***
- *AF_3DUNB?*

- **AF_MAX** prohibits dynamic additions

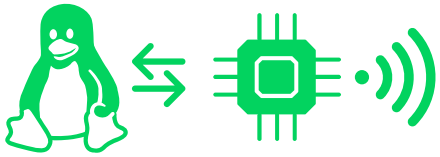
Sockets: PF_PACKET Types

- ETH_P_IEEE802154 (0x00F6)
- *ETH_P_**LORA**, ETH_P_FLRC*
  - *ETH_P_LORAWAN*
- *ETH_P_OOK(?), ETH_P_**FSK***
  - *ERP2? ZWAVE? …*
- *ETH_P_3DUNB[_{DL,UL}]?*

LoRa/FSK driver project

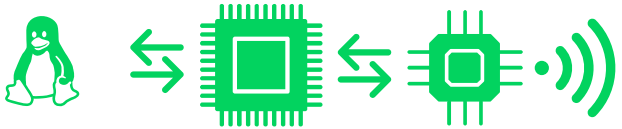
Getting Started With LoRa Chipsets

Types Of LoRa Radio Modules



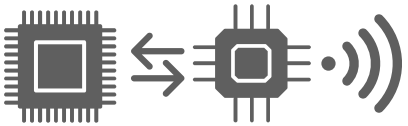
Plain transceiver

- SPI / UART / USB
- Volatile register settings
- Software MAC needed



MCU w/firmware + transceiver

- UART / USB Serial
- Firmware determines chip features exposed
- Optional certified MAC



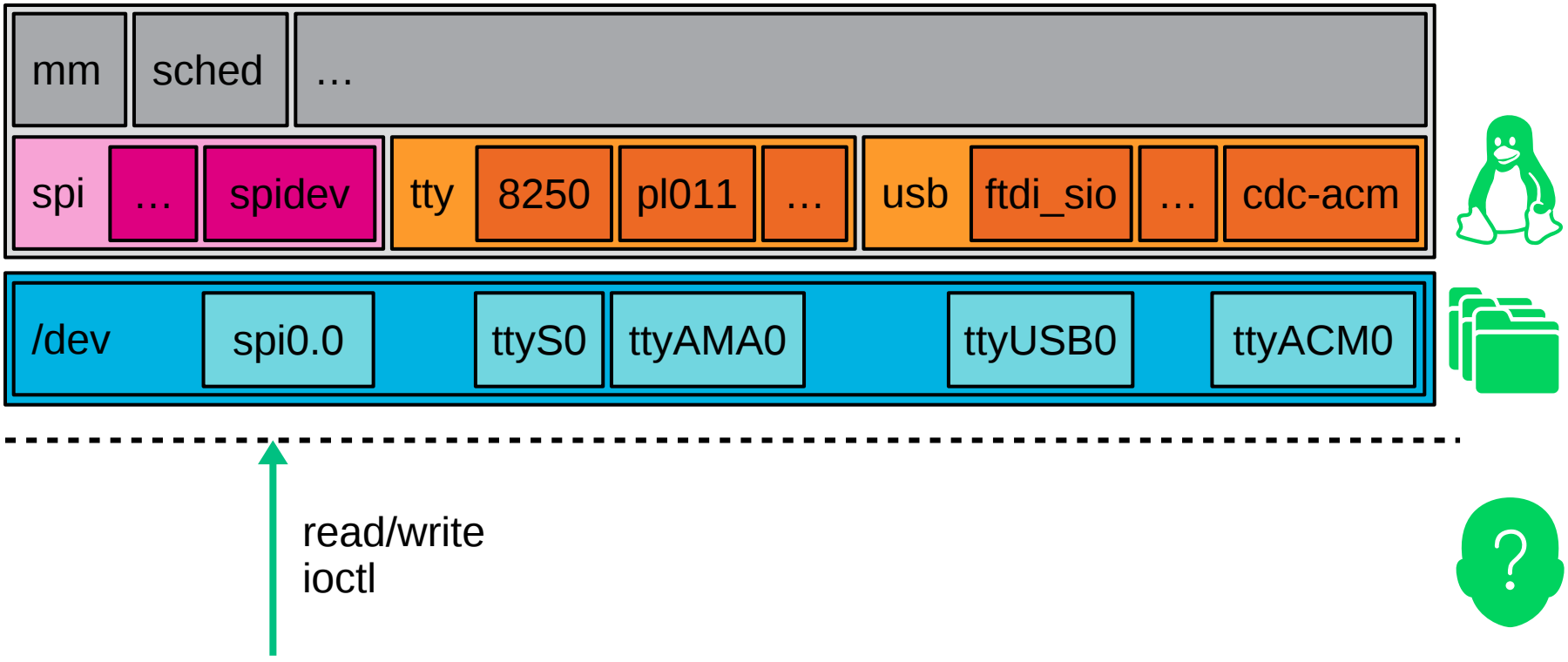
Plain MCU + transceiver

- n/a – no fixed API
- Custom MCU code for sending / receiving
- Optional MAC

LoRa Use Cases On Linux

- Prototyping of sensor/actuator node
  - Well-documented transceiver chips
  - Simple sending of raw LoRa packets
  - LoRaWAN client
- LoRaWAN gateway
  - Complex multi-channel “concentrator” chip

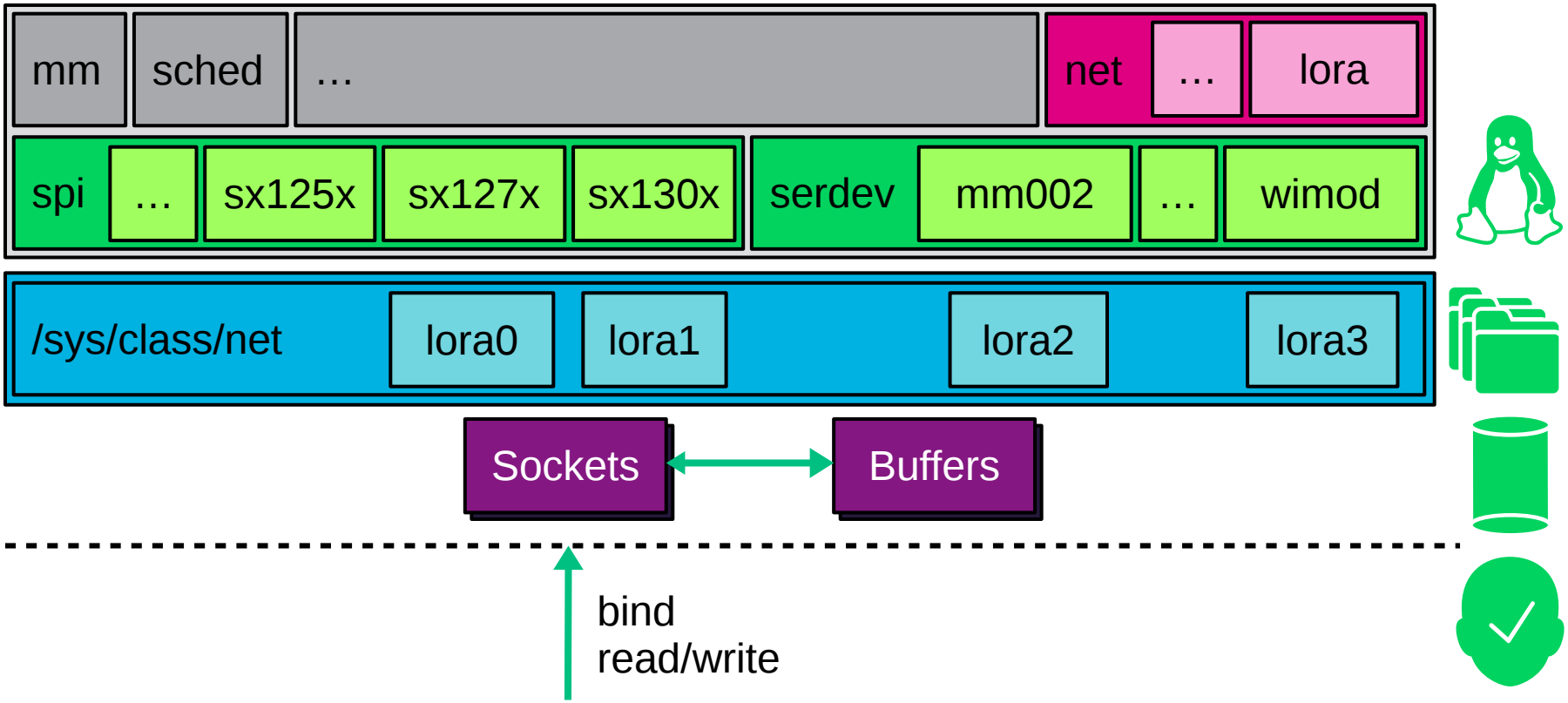
Accessing LoRa Hardware Today



Goals For LoRa On Linux

- Get vendor-independent interface
  - Hardware support via kernel, not via forks
  - Generic Open Source packages via distros
- Allow to containerize the userspace part
- Enable Kubernetes for gateway & backend

LoRa Sockets Concept



Netlink Commands / Attributes

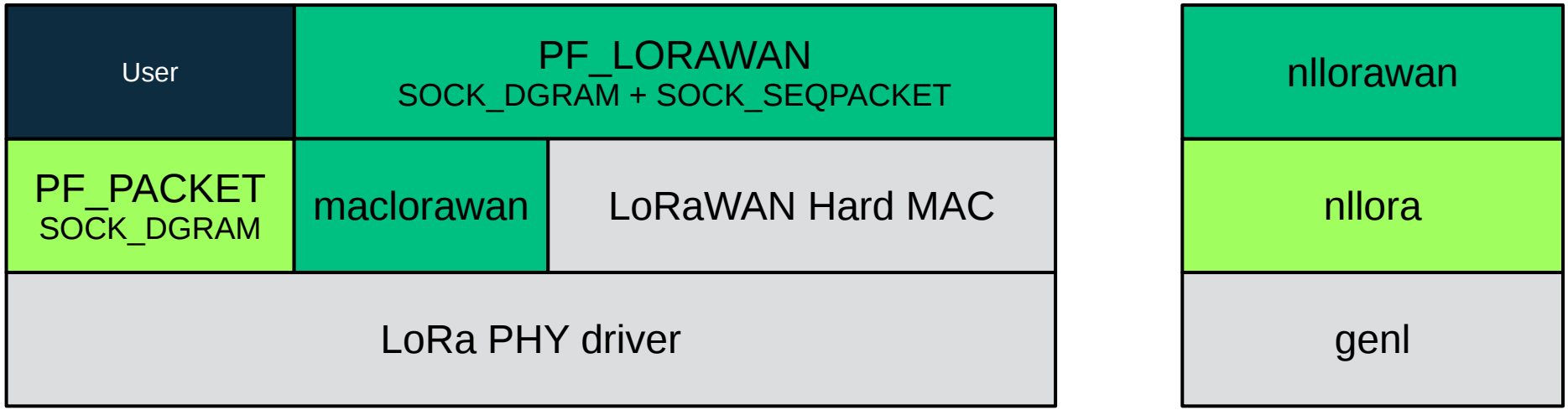
NLLORA_CMD_SET_

- U32 FREQ
- S32 TX_POWER
- *u32 bandwidth*
- *u8 sf, cr, sync_word*

NLFSK_CMD_SET_

- U32 FREQ
- U32 FREQ_DEV
- S32 TX_POWER

LoRa Socket Layers (Proposed)



Userspace Code

- test.c
  - Trivial code sending a packet
- nltest.c
  - Evolving into a dual LoRa/FSK config tool

Regmap Workaround

- Writing to FIFOs is broken in 5.3 and 5.2
  - Breaks bringing sx130x interfaces up
- Patch in progress by Ben Whitten
- Workaround: bump .max_register = 0xffff

Netdev 0x13 Workshop Outcome

- Use module param for hard- vs. soft-MAC
- Expose two devices, use carrier up/down
- Not all modes (BLE) need to be exposed
- LoRaWAN soft-MAC like 802.15.4 6LoWPAN

Help Needed!

- Netlink operations need to be defined
- Complete/add driver implementations
- Prepare and document DT snippets
- Testing
- Design discussions
- Solve various unrelated problems

Resources

- linux-lpwan mailing list
- #lora on Freenode IRC

- ELCE 2018 video and slides
- Netdev 0x13 paper, slides and video
- oSC 2019 video

Kernel development on openSUSE

Kernel Development Options

- linux.git (cross-)compile → manual deploy
- kernel-source.git → OBS → package install
- kernel-{default,lpae}-devel package
  → local module(s) or KMP package in OBS

My Interop Testing Setup

- Various Arm, MIPS boards (C-Sky TBD) with expansion boards or cables
  - No Intel or Arm ACPI yet – hardware needed
- GitHub afaerber/lora-modules.git
- kernel.org afaerber/linux-lora.git
- One sends; monitor others' dmesg output

Credits – LoRa driver project

Industry Contributors – Code

Industry Supporters – Hardware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