



Contribution ID : 239

Type : **not specified**

RISC-V Platform Specification Kick-Off

Thursday, 15 November 2018 09:00 (30)

There has been a lot of talk about requirement of a RISC-V platform specification to standardise various key components.

One of them is Platform Level Interrupt Controller (PLIC) and local interrupts. We also need a stable yet extensible firmware interface for RISC-V to support virtualization and power management extensions.

Another area that can be discussed is a standard boot flow for any RISC-V based unix platform.

I agree to abide by the anti-harassment policy

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Session Classification : RISC-V MC